

L Number	Hits	Search Text	DB	Time stamp
1	90815	interface same (processor or microprocessor or (processing near2 unit)) same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:27
2	93174	interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:54
3	39192	(memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:20
4	143158	(select\$3 or multiplex\$3) near15 (port or path)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:29
5	34457	interfac\$3 near6 (chip or asic or integrated)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:21
6	10008	(711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.) or (710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:22
7	157343	memory same (controller or interface or bridge) same (microprocessor or cpu or processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:55
8	2076	((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)) and ((select\$3 or multiplex\$3) near15 (port or path)) and (interfac\$3 near6 (chip or asic or integrated)) and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:23
9	278	((711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.) or (710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.)) and (((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)) and ((select\$3 or multiplex\$3) near15 (port or path)) and (interfac\$3 near6 (chip or asic or integrated)) and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:23

10	237	((((711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.) or (710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.)) and (((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)) and ((select\$3 or multiplex\$3) near15 (port or path)) and (interfac\$3 near6 (chip or asic or integrated)) and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor)))) and (@ad<=19990929 or @rlad<=19990929) interface with (processor or microprocessor or (processing near2 unit)) same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:54
11	60493	interface with (processor or microprocessor or (processing near2 unit)) same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:27
12	204	(((((711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.) or (710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.)) and (((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)) and ((select\$3 or multiplex\$3) near15 (port or path)) and (interfac\$3 near6 (chip or asic or integrated)) and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor)))) and (@ad<=19990929 or @rlad<=19990929) and (interface with (processor or microprocessor or (processing near2 unit)) same memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:27
13	47585	interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:28
14	179	(((((711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.) or (710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.)) and (((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)) and ((select\$3 or multiplex\$3) near15 (port or path)) and (interfac\$3 near6 (chip or asic or integrated)) and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor)))) and (@ad<=19990929 or @rlad<=19990929) and (interface with (processor or microprocessor or (processing near2 unit)) same memory)) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:28

15	7486	((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:52
16	40	((((711/5.ccls. or 711/104-105.ccls. or 711/100-101.ccls. or 711/212.ccls. or 711/1.ccls. or 711/154.ccls. or 711/170.ccls.) or (710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315-316.ccls. or 710/52.ccls. or 710/62-64.ccls. 710/11.ccls. or 710/38.ccls. or 710/66.ccls. or 710/74.ccls.))) and ((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)) and ((select\$3 or multiplex\$3) near15 (port or path)) and (interfac\$3 near6 (chip or asic or integrated)) and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor))) and (@ad<=19990929 or @rlad<=19990929)) and (interface with (processor or microprocessor or (processing near2 unit)) same memory)) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)) and (((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 16:30
17	1417	((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)) with memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:53
19	516	(((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)) with memory) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:54
20	461	(((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)) with memory) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)) and (@ad<=19990929 or @rlad<=19990929)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:54
21	335	((((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)) with memory) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)) and (@ad<=19990929 or @rlad<=19990929)) and ((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:55
22	93	(interfac\$3 near6 (chip or asic or integrated)) and (((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)) with memory) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)) and (@ad<=19990929 or @rlad<=19990929)) and ((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:55

23	27562	(memory with controller)with (interface or bridge)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:57
24	59	((interfac\$3 near6 (chip or asic or integrated)) and ((((((select\$3 or multiplex\$3) near15 (port or path)) with (processor or microprocessor or (processing near2 unit)) with memory) and (interfac\$3 with (processor or microprocessor or (processing near2 unit)) with memory)) and (@ad<=19990929 or @rlad<=19990929)) and ((memory near3 (controller or interface)) and (interfac\$3 same (processor or microprocessor or (processing near2 unit)) same memory)))) and ((memory with controller)with (interface or bridge))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/09 17:57